

Rajarambapu Institute of Technology, Rajaramnagar

(An Autonomous Institute, affiliated to Shivaji University, Kolhapur)

Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Class: S. Y. B. Tech

Semester: III

Class.	S. T. D. TCCII	α .	neme Evaluation Scheme								
1		Te	achin	g Sc.	heme						
Course					t s	e		ry Ma			ical Marks
Code	Course	L	T	P	Credits	Scheme	Max	Min		Max	Min for
					ن	Scl		passing			passing
		-	-	-	-	TOP	20	(%)		<u> </u>	(%)
	Engineering					ISE	20	1,0			
SH2113	Engineering Mathematics –III	3	1	-	4	UT1	15	40	40		
	Wathematics –III					UT2	15	10			
		-	├—	-	+	ESE	50	40	-		
						ISE	20				
EC2013	Electronic Devices	3			3	UT1	15	40	40		
						UT2	15				
			<u> </u>			ESE	50	40			
						ISE	20	40	40		
EC2033	Digital System Design	3			3	UT1	15				
	g 2 co.g		1000000		-	UT2	15		40		
						ESE	50	40			
						ISE	20				
EC2053	Analog Communication	3			3	UT1	15	40	40		
					-	UT2	15		40		
						ESE	50	40			
		3				ISE	20	40			
EC2073	Network Theory				3	UT1	15		40		
	Trestroin Incorp					UT2	15		40		
						ESE	50	40			
EC2093	Electronic Devices Lab			2	1	ISE				50	50
		L			1	ESE				50	50
EC2113	Digital System Design Lab			2	1	ISE				50	50
					1	ESE				50	50
EC2133	Analog Communication Lab			2	1	ISE				50	50
						ESE				50	50
EC2153	Technical Aptitude-I	-	-	2	1	ESE				100	50
	Open Elective- II	_	_	2	1	ISE	-	-	-	100	50
	Choice Based Soft Skill Program-I				1						
	TOTAL	15	01	1	21						
				0							

ISE = In Semester Evaluation, UT1 = Unit Test-1, UT2 = Unit Test-2, ESE = End Semester Examination

Total Contact Hours/week : 26 Total Credits : 21

Technical Aptitude Courses: Engineering Mathematics –III, Electronic Devices, Digital System Design, Analog Communication, Network Theory.





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Department of Electronics and Telecommunication Engineering

Open Elective II

Sr. No.		Subject Name	Course Code
1.	Choice Based Soft	Personal Effectiveness & Body	SH2593
	Skill Program-I & II	Language	
2.		Interpersonal Skills (Work life	SH2613
		Balance)	
3.		Leadership & Public Speaking	SH2633
4.		Innovation Tools and Methods for	SH2693
		Entrepreneurs	
5.		German Language – Basic Level	SH2733
6.	18.	Japanese Language – Basic Level	SH2713

Note:

- 1. A student has to complete any two courses out of six choices offered under Choice Based Soft Skills Programme. A course in each semester will be allocated without any repetition.
- 2. The students who have completed 'German Language Lab' or 'Japanese Language Lab' in F.Y. B. Tech should not give their choice for 'German Language Basic Level' and 'Japanese Language Basic Level'. Such students may give their choices for 'German Language Advanced Level' and 'Japanese Language Advanced Level' (batch sizes 40 each) in the S.Y. B. Tech Sem-IV only.
- 3. The students who will select and will successfully complete 'German Language Basic Level' and 'Japanese Language Basic Level' in S.Y. B. Tech Sem-III will by default (mandatorily) appear for Advance Levels of said courses in Semester-IV.





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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Class: S. Y. B. Tech

Semester: IV

Class.	S. 1. D. Teen			-				501	nester	• 1 4	
				achin hemo				Evalu	ation S	Scheme	
Course Code	Course				ts	ne ne	The	Theory Marks		Pra	ctical Marks
Code		L	Т	P	Credits	Scheme	Max	pas	n for sing %)	Max	Min for passing (%)
						ISE	20	,	ľ		
CE2263	Engineering Mechanics	3	_	_	3	UT1	15	40	10		
CDZZOS	Engineering ivicenames	3	-	-)	UT2	15		40		
						ESE	50	40			
						ISE	20				
EC202	Linear Integrated Circuit and	3			3	UT1	15	40	40		
	Applications				3	UT2	15		40		
						ESE	50	40			
						ISE	20				
EC2043 Microcontrollers		3			3	UT1	15	40	40		
		-				UT2	15		40		
						ESE	50	40			
						ISE	20				
EC2063	Digital Communication	3			3	UT1	15	40	40		
						UT2	15		40		
						ESE	50	40			
F. C2 2 2 2						ISE	20				
EC2083	Signals and System	3			3	UT1	15	40	40		
					le i	UT2	15		40		
						ESE	50	40	- Tile		
SH2173	Environmental Science	1*	_	_	1	ISE	50	40	40		
CECCO	T					ESE	50	40	40		
CE2283	Engineering Mechanics Lab	-	-	2	1	ISE				100	50
EC210	Linear Integrated Circuit and			2	1	ISE				50	50
	Applications Lab					ESE				50	50
EC2123	Microcontrollers Lab			2	1	ISE				50	50
					1	ESE				50	50
EC2143	Digital Communication Lab			2	1	ISE				50	50
						ESE				50	50
SH2603	Environmental Science Project	-	-	2*	1	ISE				100	50
EC2163	Technical Aptitude-II	-	-	2	1	ESE				100	50
	Open Elective- III	_	_	2	1	ISE	-	-		100	50
	Choice Based Soft Skill Program-II										
TOP T C	TOTAL 16 - 14 23										*

ISE = In Semester Evaluation, UT1 = Unit Test-1, UT2 = Unit Test-2, ESE = End Semester Examination

Total Contact Hours/week
Total Credits

: 30 : 23

Note*: One extra lecture to be allotted to Environment Science in time table.

Technical Aptitude Courses: Linear Integrated Circuit and Applications, Microcontrollers, Digital Communication, Signals and System.





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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Open Elective III

Sr. No.		Subject Name	Course Code		
1. Choice Based Soft Skill Program-I & II		Personal Effectiveness & Body Language	SH2593		
2.		Interpersonal Skills (Work life Balance)	SH2613		
3.		Leadership & Public Speaking	SH2633		
4.		Innovation Tools and Methods for Entrepreneurs	SH2693		
5.		German Language - Advanced Level	SH2643		
6.		Japanese Language - Advanced Level	SH2623		

Note:

- 1. A student has to complete any two courses out of six choices offered under Choice Based Soft Skills Programme. A course in each semester will be allocated without any repetition.
- 2. The students who have completed 'German Language Lab' or 'Japanese Language Lab' in F.Y. B.Tech should not give their choice for 'German Language Basic Level' and 'Japanese Language Basic Level'. Such students may give their choices for 'German Language Advanced Level' and 'Japanese Language Advanced Level' (batch sizes 40 each) in the S.Y. B. Tech. Sem-IV only.
- 3. The students who will select and will successfully complete 'German Language Basic Level' and 'Japanese Language Basic Level' in S.Y. B.Tech Sem-III will by default (mandatorily) appear for Advance Levels of said courses in Semester-IV.



OFFICE STREET

K.E. Society's

Rajarambapu Institute of Technology, Rajaramnagar

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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Class: T. Y. B. Tech

Semester: V

	5. 1. 1. D. 1001					Semester: V						
		T	'each	ing S	Scheme		Evaluation Scheme					
Course					100	به	Th	eory Ma	rks	Pr	actical Marks	
Code	Course	L	T	P	Credits	Scheme	Max		n. for ng (%)	Max	Min. for passing (%)	
			1			ISE	20					
EC3013	Digital Signal Processing	3			3	UT1	15	40	40			
	g1g					UT2	15		40			
			_	_		ESE	50	40				
						ISE	20	1				
EC3033	Control Systems	3			3	UT1	15	40	40			
						UT2	15		_ +0			
		-	—	<u> </u>		ESE	50	40				
						ISE	20					
EC3053	CMOS VLSI Design	3			3	UT1	15	40 40	40			
4						UT2	15					
-						ESE	50	40				
	Electromagnetic Waves &					ISE	20	4				
EC3073		3			3	UT1	15	40	40			
	Antenna Theory					UT2	15		_ 40			
<u></u>		-	-	_		ESE	50	40				
						ISE	20					
	Program Elective-I	3	-	_	3	UT1	15	40	40			
						UT2	15		1 70			
	Digital Signal Processing Lab	-	_	-		ESE	50	40				
EC3213				2	1	ISE		-		50	50	
	using MATLAB					ESE		-		50	50	
EC3233	CMOS VLSI Design Lab			2	1	ISE		-	-	50	50	
EG2252						ESE		-	-	50	50	
EC3253	Antenna Lab			2	1	ISE		_	_	100	50	
						ESE		_	-	50	50	
EC3273	Object Oriented Programming					ISE			-	100	50	
	using C++ Lab			2	1	ESE				50	50	
EC3293	Technical Aptitude-III	-	-	2	1	ESE		_	-	100	50	
						ISE	20					
CITACAA	2022					UT1	15	40				
SH3033	Scholastic Aptitude I	2*	-	-	2	UT2	15	1	40			
						ESE	50	40				
7000	Summer Internship**						30	40				
EC3313	(4 Weeks)	-	-	-	2	ISE				100	50	
SH3011	Indian Constitution	2		-	Audit	ISE	100	50	(DAT)			
	TOTAL	19	-	10	24	ISE	100	50	(P/N)			
TOT T		.,		10	47							

ISE = In Semester Evaluation, UT1 = Unit Test-1, UT2 = Unit Test-2, ESE = End Semester Examination

Total Contact Hours/week

Total Credits

: 29 : 24

Note*: One extra lecture to be allotted to Aptitude Training-I in time table.

Technical Aptitude Course: Digital Signal Processing, Control Systems, CMOS VLSI Design, Electromagnetic Waves & Antenna Theory.

Note**: Students are required to undergo industrial / field training of minimum four weeks in the vacation of Semester-IV and its evaluation will be carried out in the Semester-V





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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch] Department of Electronics and Telecommunication Engineering

Program Elective-I

Sr.No	Course Code	Domain	Course
1.	EC3093	Communication	Information Theory and Coding
2.	EC311	Communication	Wireless Communication
3.	EC313	VLSI and Signal	VLSI testing
4.	EC315	Processing	Soft Computing
5.	EC317	Embedded	Automotive Electronics
6.	EC319	Systems and Automation	Advanced Microcontrollers Architecture





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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Class: T. Y. B. Tech

Semester: VI

			Te	achin	g			NOTE TO THE PERSON NAMED IN COLUMN NAMED IN CO	Semesti		-
				heme				Evalua	ation Sche	me	
Course Code	Course				ts	ge de	Theory N	Marks		Pract	ical Marks
Couc		L	Т	P	Credits	Scheme	Max	Min. for passing (%)		Max	Min. for passing (%)
E02022						ISE	20				
EC3023	Power Electronics	3			3	UT1	15	40	40		
	Power Electronics		1			UT2	15				
		+	+	-	-	ESE	50	40			
EC3043		3			2	ISE UT1	20 15	1			
2030.3	Machine Learning	3			3	UT2	15	40	40		
						ESE	50	40	-		
		-	+			ISE	20	40			
)	D 71					UT1	15	40			
	Program Elective-II	3	-	-	3	UT2	15	- 40	40		
						ESE	50	40	1		
						ISE	20	10		 	
	Open Elective-IV	3	_	_	3	UT1	15	40			
	Open Dicenve-1 v	3	-	-	3	UT2	15		40		
						ESE	50	40			
						ISE	20				
SH3021	Biology for Engineers	3	-	_	3	UT1	15	40	40		
	6,8				3	UT2	15		40		
EC3183	m 1 : 1 1 : 1 : 1 : 1 : 1					ESE	50	40			
EC3183 EC3203	Technical Aptitude-IV	-	-	2	1	ESE				100	50
EC3203	Power Electronics Lab			2	1	ISE				50	50
	Electronics Product		-			ESE				50	50
EC3223	design Lab			2	1	ISE				50	50
						ESE				50	50
EC3243	Python Programming			2	1	ISE				50	50
) 1 1000 (0	Lab					ESE				50	50
EC3263	Capstone Project phase-I	-	-	2	2	ISE		-		100	50
SH304	Psychology for	1*	_	_	1	ISE	50		10		and the second
	Engineers	_				ESE	50	4	10		
						ISE	20	1			
SH3043	Scholastic Aptitude II	2*	-	-	2	UT1	15	40	40		
						UT2	15		40		
						ESE	50	40	İ		
ICE - I-	TOTAL	18		10	24						

ISE = In Semester Evaluation, UT1 = Unit Test-1, UT2 = Unit Test-2, ESE = End Semester Examination

Total Contact Hours/week: 28

Total Credits

: 24

Note*: One extra lecture to be allotted to Aptitude Training-II and Psychology for Engineers in time Table.

Technical Aptitude Courses: Digital Signal Processing, Power Electronics, CMOS VLSI Design, Electromagnetic Waves & Antenna Theory, Control systems.





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Department of Electronics and Telecommunication Engineering

Program Elective - II

Sr.No	Course	Domain	Course					
	Code	× .	- Acceptance of the Control of the C					
1.	EC306	Communication	Microwave Theory and Techniques					
2.	EC308	Communication	Wireless Sensor Network					
3.	EC310		RTL Simulation and Synthesis with					
9-1		VLSI and Signal Processing	PLDs					
4.	EC312		Digital Image Processing					
5.	EC314	Embedded Systems and	Robotics					
6.	EC316	Automation	Numerical Processors					

Onen Elective-IV

			Open Elective-IV
Sr. No.	Branch	Course Code	Open Elective-IV Courses
1	Automobile	OE3023	Reliability Engineering
2	Automobile	OE3043	Renewable Energy Sources
3	Civil	OE3063	Environmental Impact Assessment
4	Civil	OE3083	Material Management
5	Computer	OE3103	Network Administration
6	Computer	OE3123	Information Technology Foundation Program
7	E&TC	OE3143	Mechatronics
8	Electrical	OE3163	Engineering Materials
9	Electrical	OE3181	Industrial Drives
10	CS&IT	OE336	Neural Network and Deep Learning
11	CS&IT	OE3221	Cyber Forensics
12	MBA	OE3243	Marketing for Engineers
13	Mechanical	OE3263	Aircraft Systems
14	Mechanical	OE3283	Supply Chain Management
15	Mechanical	OE3301	New Product Design and Development
16	Mechanical	OE3323	Entrepreneurship Development
17	Mechanical	OE3341	Research Methodology





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With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Class: Final Year B. Tech Semester: VII

	Time rear B. 1		T	acl	hing	OT.					Seme	ster: VII			
			S	che	me	5			Evaluation Scheme						
Course Code	Course					ts	e	Theor	y Marks		Practi	cal Marks			
Code	Code Course L T P Supply Suppl		Max.	Min. fo		Max.	Min. for passing (%)								
							ISE	20							
EC4013	Computer Network	3	-	.	_	3	UT1	15	40	10					
	1					5	UT2	15		40		T			
							ESE	50	40						
							ISE	20							
EC4031	Artificial	3	_	1.	_	3	UT1	15	40	10					
	Intelligence					3	UT2	15		40					
		-		ESE	50	40									
	Program Elective-						ISE	20				T			
	III	3			.	3	UT1	15	40	10		—			
							UT2	15		40					
		_			\perp		ESE	50	40	Ī					
	Program Elective-						ISE	20							
	IV	3	-	_		3	UT1	15	40	10					
							UT2	15		40					
		_	_	_	\perp		ESE	50	40						
							ISE	20							
	Program Elective-V	3	-	-		2 L	UT1	15	40	40					
							UT2	15		40					
	Computer Net 1				1		ESE	50	40						
EC4533	Computer Network lab	-	-	2		1	ISE			•	100	50			
	Program Elective-	_	_	2		1 –	ISE				50	50			
	IV Lab			_		-	ESE			8	50	50			
EC4553	Capstone Project Phase-II	_	-	4	4	4	ISE				100	50			
	TOTAL				_		ESE				100	50			
GD .	moster Frank L. V.	1 5	0	8	2	1						N (02)			

ISE = In Semester Evaluation, UT1 = Unit Test-1, UT2 = Unit Test-2, ESE = End Semester Examination

Total Contact Hours/week : 23 Total Credits : 21





Rajarambapu Institute of Technology, Rajaramnagar (An Autonomous Institute, affiliated to Shivaji University, Kolhapur) Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch] Department of Electronics and Telecommunication Engineering

Program Elective-III

Course Code	Domain	Course				
EC4053	0	Satellite Communication				
EC407	Communication	Multimedia communication				
EC409	VLSI and Signal	Computer Vision				
EC4111	Processing	Mixed Mode VLSI Design				
EC4133	Embedded Systems and	Embedded Linux				
EC415	Automation					
EC+13	Automation	Advanced Power Electronics				
֡	EC4053 EC407 EC409 EC4111 EC4133	EC4053 EC407 Communication EC409 VLSI and Signal EC4111 Processing EC4133 Embedded Systems and				

Program Elective-IV

Sr.No	Course Code	Domain	Course					
1.	EC417	0	Radar and Optical Fiber Communication					
2.	EC4191	Communication	Internet of Things					
3.	EC4213	VLSI and Signal	System Verilog					
4.	EC4233	Processing	Biomedical Electronics					
5.	EC425	Embedded Systems and	Embedded System Design					
6.	EC4273	Automation	Programmable Logic Controller & SCADA					

Program Elective-V

Sr.No	Course Code	Domain	Course					
1.	EC4291	G	Network Security					
2.	EC431	Communication	Pattern Recognition					
3.	EC4332	VI CI 1C'	Low Power VLSI					
4.	EC435	VLSI and Signal Processing	Digital Signal Processing For Wireless Communication					
5.	EC4372	Embedded Systems	Computer Architecture					
6.	EC4392	and Automation	Electric Drives and Control					

Program Elective-IV Lab

Sr.NoCourse CodeDomainCourse1.EC441CommunicationRadar and Optical Fiber Communication2.EC443Internet of Things3.EC4453VLSI and SignalSystem Verilog4.EC447ProcessingBiomedical Electronics5.EC449Embedded Systems and AutomationEmbedded System Design6.EC4513AutomationProgrammable Logic Controller & SCADA		am Elective-1v	Lab								
1. EC441 2. EC443 3. EC4453 4. EC447 5. EC449 Communication Radar and Optical Fiber Communication Internet of Things System Verilog Biomedical Electronics Embedded Systems and Embedded System Design	Sr.No	Course Code	Domain	Course							
2. EC443 3. EC4453 VLSI and Signal 4. EC447 Processing Embedded Systems and Embedded System Design System Verilog Biomedical Electronics Embedded Systems and Embedded System Design	1.	EC441	Committee								
3. EC4453 VLSI and Signal System Verilog 4. EC447 Processing Biomedical Electronics 5. EC449 Embedded Systems and Embedded System Design	2.	EC443	Communication								
4. EC447 Processing Biomedical Electronics 5. EC449 Embedded Systems and Embedded System Design	3.	EC4453	VLSI and Signal								
5. EC449 Embedded Systems and Embedded System Design	4.	EC447									
6 EC4512	5.	EC449	Embedded Systems and								
Programmable Logic Controller & SCADA	6.	EC4513									
				Programmable Logic Controller & SCADA							





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Curriculum Structure and Evaluation Scheme

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Department of Electronics and Telecommunication Engineering

Track I: Industry Internship & Project (IIP)

Class: Final Year B. Tech

Semester: VIII

			Teaching Scheme				Evaluation Scheme					
Course Code	Course	L			I;	E	Theory Marks			Practical Marks		
	1		Т	P	Credit s	Schem	Max.	Min. fo		Max.	Min. for passing (%)	
OE4381	Finance for Engineers (Online Course)	2	-	-	2	ISE	25	40	40			
						ESE	75	40				
OE4361	Engineering Management & Economics	2	-	-	2	ISE	25	40	40			
	(Online Course)					ESE	75	40				
IP4023	Internship & Project		_	_	8	ISE				50	50	
11 1025	monship & Hojeet		10000		0	ESE			-	50	50	
	TOTAL	4	-	-	12							

ISE = In Semester Evaluation, ESE = End Semester Exam

Total Contact Hours/week

: 04

Total Credits

: 12

Note:

- 1] Weekly Contact hours are not mentioned for IP4023 course as student is expected to be in industry regularly for 20 weeks. However, student needs to report to Institute mentors as and when required.
- 2] For online course, lecture videos of each unit will be made available through college platform to the students. For each unit there will be separate assignment. Students need to submit all assignments within specified time.

Weightage: 25% weightage for unit wise assignments + 75% weightage for final exam. Final exam will be held at college campus.





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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch] Department of Electronics and Telecommunication Engineering

Track II: Undergraduate Research Experience (URE)

Class: Final Year B. Tech

Semester: VIII

Course Code			Teaching Scheme				Evaluation Scheme					
	Course	L			dit	ша	Theory Marks			Practical Marks		
			Т	P	Credit	Schem	Max.	Min. fo		Max.	Min. for passing (%)	
OE4381	Finance for Engineers (Online Course)		2 -	-	2	ISE	25	40	40			
	,					ESE	75	40				
OE4361	Engineering Management &	2	-	-	2	ISE	25	40	40			
	Economics (Online Course)					ESE	75	40				
RE4043	Research Project		_	8	8	ISE				50	50	
1010			2000	U	3	ESE			-	50	50	
	TOTAL			8	12							

ISE = In Semester Evaluation, ESE = End Semester Exam

Total Contact Hours/week

: 12

Total Credits

`: 12

Note:

1] For online course, lecture videos of each unit will be made available through college platform to the students. For each unit there will be separate assignment. Students need to submit all assignments within specified time.

Weightage: 25% weightage for unit wise assignments + 75% weightage for final exam. Final exam will be held at college campus.





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Curriculum Structure and Evaluation Scheme

With effective from 2020-21 [2020-24, 2021-25 Batch]

Department of Electronics and Telecommunication Engineering

Track III: Entrepreneurship Development (ED)

Class: Final Year B. Tech

Semester: VIII

Class.		_		_		Semester: VIII						
	Course	Teaching Scheme			Evaluation Scheme							
Course Code		L			Credits	Scheme	Т	heory Ma	rks	Practical Marks		
			T	P		Scho	Max	Min. passin		Max	Min. for passing (%)	
			-		2	ISE	20		40	-		
ED4103	Project Management	2*		-		UT-1	15	40				
22 1103						UT-2	15				-	
						ESE	50	40				
	Commercial Aspects of the Project	2*	-	-	2	ISE	20			-		
ED4043						UT-1	15	40	40			
LDTOTS						UT-2	15		40		-	
-						ESE	50	40				
ED4063	Entrepreneurship Development Program (EDP)	-		-	1	ISE	-	-	-	100	50	
ED4083	Entrepreneurship Development Project	-	-	7	7	ISE	-	-	-	50	50	
				_	\dashv	ESE				50		
	Total			7	12			-	-	1-		

ISE = In Semester Evaluation, UT1 = Unit Test-1, UT2 = Unit Test-2, ESE = End Semester Examination

Total Contact Hours/week

: 11

Total Credits

: 12

Note

1] * One extra lecture to be allotted to Project Management and Commercial Aspects of the Project course in time table.

