



An Alumni News Letter

# RIT CommuniQue

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## One Step toward Green Revolution



RIT celebrated Tree Plantation Programme on 4<sup>th</sup> July, 2017 to be a part of “One Step toward Green Revolution” campaign. This programme was organized by the NSS Department & RIT Alumni Association. The programme has honored by the presence of Dr. Mrs. S. S. Kulkarni (Director), Mr. Nazir Shaikh (Chairman, Alumni Association), Dr. S. M. Sawant (Dean, Student Development), Prof. P. M. Mohite (Dean, Infrastructure), Dr. A. C. Attar (Dean, Quality Assurance), Dr. D. G. Thombare, Dr. S. S. Gawade (Chief Alumni Officer), Prof. Y. M. Patil (Secretary, Alumni Association), Prof. Sandip Patil (Program Officer, NSS), and all other teaching & non teaching staff. All the dignitaries, teaching & non teaching staff planted 180 saplings on this occasion. The tree plantation campaign is taking the shape of a movement. Every citizen should plant a tree as his or her duty.

## Distinguished Alumnus



**Name:-** Mr. Rajendra Ranmale  
**Branch:-** Electronics Engineering  
**Year of Passing:-** 1998  
**Institute :-** RIT, Rajaramnagar

Design Engineer, Senior Principal at Broadcom  
 Broadcom , Bengaluru Area, India

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Mr Rajendra completed his post-graduation in Electronics and Communications from VJTI Mumbai in year 2000. He joined Texas Instruments, Bangalore in 2000, having cleared UPSC IES examination with all India rank of 41. At Texas Instruments, as a VLSI designer, he contributed to design of state of the art DSL CPE modems of the time. He moved to US for a short period and contributed to design of DSL CO modems with unparalleled level of integration, which consists of several TI DSP processors on a single chip. During his work at TI, along with the design work, he streamlined various design flows which helped in faster chip tape-outs. He published a paper based on his work in formal verification in 2002. This paper was presented in VDAT 2002 IISc Bangalore, and IEEE ASIC SoC Conference 2002 in the United States.

Mr Rajendra joined Broadcom Corporation Bangalore in 2004. He has since actively worked in various roles in the company. He started with working in flow automation, where he introduced various new techniques to optimize designs. He has led several projects to design display drivers in hardware (SD/HD/4K/3D) for set top boxes and digital TVs. All the major set top box vendors/carriers in India use these chips in their boxes. He has highly contributed to Broadcom's DSL CO designs where his work included design of network buffer management and DSP processor caches.

Mr Rajendra managed a team which contributed to the lowest power IOT WiFi chip designs, bringing out industry best WiFi chips for IOT market focused on power savings.

Current work of Mr Rajendra includes design of 3D graphics processors. He leads a team in Bangalore that designs these highly parallel processors and caches. His work has enabled several key chip products from Broadcom.

Mr Rajendra is considered expert in VLSI design, covering all areas of frontend VLSI design and verification, and is recognized by his company several times.



**Name:-** Mr. Jagdish S. Zagade  
**Branch:-** Electronics Engineering  
**Year of Passing:-** 2001  
**Institute :-** RIT, Rajaramnagar  
**Email :** [jagdishzagade@gmail.com](mailto:jagdishzagade@gmail.com)  
**Project Manager**

**Larsen & Toubro Limited**  
 2035 Lincoln Highway # 3000  
 Edison, NJ 08817

After graduating from RIT Rajaramnagar in 2001, got an opportunity to study more on cutting edge technologies and got chance to play with very high tech machines. He shared his experiences in his own words as "Am very proud and happy for getting chance to support, Boeing 787-8 Dreamliner electric system (Long range wide body aircraft) Amazing American experience in Professional as well as personal life from last 6 years".

Additionally, he is the editor for vertical newsletter published in Larsen and Toubro Limited (USA, UK and India branches).



## Alumni Interactions

### Department of Management Studies

Mrs. Alaka Raj – Sooraj presentably residing at Sultanate of Oman, UAE visited the Institute on 23<sup>rd</sup> June 2017. She is renowned alumnus of Mgt. Studies Department (MBA) (Pass Batch 2001). During this visit she interacted with MBA students & department faculties. She also shared her past learning experience with the students & faculties. Mrs. Alaka Raj – Sooraj was felicitated for her success so far by Dr. Mrs. Shushma Kulkarni (Director, RIT), Dr. M. T. Telsang (Dean, Academics), Dr. S. M. Sawant (Dean, Students Development), Dr. S. S. Gawade (Chief Alumni Officer), Dr. Mrs. H. V. Gaikwad (HOD, MBA).



A Test Autothon is like a Hackathon but for testing professionals where participants had to automate a business case within limited time to win a whole load of prizes (including cash prizes).

The objective/problem statement was to set up an automated testing framework (API automation, Web automation, mobile automation and performance testing) in a continuous deployment mode on AWS platform, for a buggy application (Web UI & Mobile), the source code of which, was checked in and given to all contestants

The contest was held at NOVOTEL (Nagar Road, Pune) on 29<sup>th</sup> June, 2017.

### Mechanical Engineering department

Our renowned alumni Mr. Ravikant Verma, (1989-90 batch) from Mechanical Engineering, visited our institute, on 6<sup>th</sup> May, 2017. He worked as Quality Head & MR TS EMS OHSAS, Plant Head & MR, Rudrapur at Syndicate Wiper & Exhaust system, Executive Quality Engineer, at Fiat Automobiles India Limited, Mumbai, India.

### Department of Information Technology



Our alumni Mr. Chintamani Manjare, (2013 pass out) participated on behalf of "Talentica Software" and won the coveted 1<sup>st</sup> Prize of One lakh Rupees of 'Test Autothon 2017' powered by Amazon Web Services.



## Alumni Meet at RIT



RIT has organized alumni get together on 8<sup>th</sup> April 2017. This program has honored by the presence of Dr. Mrs. S. S. Kulkarni (Director, RIT), Dr. S. M. Sawant (Dean, Students Development), Dr. S. S. Gawade (Chief Alumni Officer) & all department alumni coordinators. The program started with welcome & review of alumni activities in the year 2016-17. Dr. S. M. Sawant has explained the plan for silver jubilee batch alumni get together of 1992 batch & previous batches. Dr. Mrs. S. S. Kulkarni has talked with present alumni, she has appealed to alumni to be in touch with institute and support for overall institute development. Finally the open discussion session was conducted for enhancing alumni interactions. Vote of thanks of the program was proposed by Prof. D. T. Mane.

## National Level Technical Symposium

Quantum 2K17, a National Level Technical Symposium, inaugurated on 22<sup>nd</sup> April, 2017 in the presence of Mr. Jitendra M. Patil, (M. Tech., IIT, Mumbai), Associate Vice President R&D, Artillery head, Bharat Forge Ltd., Pune). In this Technical Symposium events named Project Master, Nav-nirmiti, Nirmiti & Ankuran were held.

- **In Project Master event**, final year B. Tech. student's project competition took place. This project master event was organized by all the seven departments of RIT. We had total of 153 project groups and 610 students presented their project models and posters.
- **Nirmiti** event was a science exhibition event for high school students. A total of 47 groups and 94 students participated in this event. Secondary school students from various schools such as Miraj taluka, Walwa taluka, Shirala taluka and Kolhapur presented their project models.
- **Nav-Nirmiti** event was a mini-project competition for First Year B. Tech students. There were 163 project groups and 510 students presented their project models.
- **Unnat Ankuran** event was a competition to deal with societal issues. For this we had 10 groups and participation of 31 students. In Unnat Ankuran, some topics were highlighted such as: Education quality (Government schools) Women Empowerment, Solid Waste Management, Electronic waste Management, Road accidents, Sanitation, Non-renewable Energy.

## Congratulation and Best wishes



Prof. A. C. Adamuthe awarded Ph. D. in Technology (degree in Information Technology) by Mumbai University, Mumbai under the guidance of Dr. G. T. Thampi. Title of thesis is Forecasting cloud computing life cycle.



Dhananjay G. Thombare, working as Professor in Automobile Engineering Department of RIT awarded Ph. D in Mechanical Engineering by Shivaji University, Kolhapur on 12<sup>th</sup> June, 2017. The title of his research is 'Investigation of Biomass Stirling Engine'. He received research guidance from Dr. Suhas V. Karmare, Professor, Govt. College of Engineering, Awasari, Pune. Dr. Dhananjay worked on utilisation biomass fuel for power generation for remote and rural location where electricity is not accessible. Dr. Dhananjay has published 17 research papers on his research in International, National Journals and Conferences.



Mr. Digvijay Patil (2015-16 B. Tech Mechanical batch) got admission in Illinois Institute of Technology, Chicago Fall 2017 under the program: PhD in Industrial engineering. He has got GRE score 300/340 and IELTS band is 6.5/9 on this basis he applied for 4 foreign Universities out of which, he got admission in Wichita State University and Illinois Institute of Technology.



Mr. Tausif Mansur Mulla (2012-13, M.E. Mech. Design) got admission in Inha University, Incheon, South Korea for Program: PhD in Mechanical engineering with major in Vibration Control, and also got full Jangsoak

Scholarship.

### Get together of Silver Jubilee batch

RIT has planned get together of silver jubilee batch (Pass out year **1992**) with their senior alumni on Saturday, **12<sup>th</sup> August, 2017**. Please reserve your day and date for this mega event.

### Editorial Board

**Dr. S. M. Sawant (Dean, Student Development)**  
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